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FARATEIF POUYA TECHNOLOGY

FTP-FMCADC4-EBZ-01

Introduction

The AD-FMCADC4-EBZ is a high speed 4-channel data acquisition board featuring two AD9680 dual channel ADC at 1000 MSPS and four ADA4961 low distortion, 3.2 GHz, RF DGA driving each converter. The FMC form factor supports the JESD204B high speed serial interface. All clocking and channel synchronization is support on-board using the AD9528 clock generator.

Features

- Four channels of 1.0 GSPS conversion utilizing JESD204B high speed serial interface
- Driver amplifier interface with 21dB voltage gain adjustment
- Optional on-board or external clocking
- Specific design and I/O added for multi-board synchronization
- AD9680 14-bit dual channel ADC
- SFDR at 1 GSPS = 85 dBFS at 340 MHz, 80 dBFS at 1 GHz
- SNR at 1 GSPS = 65.3 dBFS at 340 MHz, 60.5 dBFS at 1 GHz
- ENOB = 10.8 bits at 10 MHz
- 2 GHz usable analog input full power bandwidth

Applications

- Wide assortment of military applications, such as radar and jamming/anti-jamming
- Communications
- Diversity multiband, multimode digital receivers
- 3G/4G, TD-SCDMA, W-CDMA, GSM, LTE
- General-purpose software radios
- Ultrawideband satellite receivers
- Signals intelligence (SIGINT)
- HFC digital reverse path receivers