1110, Commercialization & Technomart Complex

Pardis Technology Park, Tehran, Iran

Tel:+98 21 76250995 Fax:+98 21 76250998

Email: info@farateif.com

# Multi Transceiver Board FTP-KP788-01-2



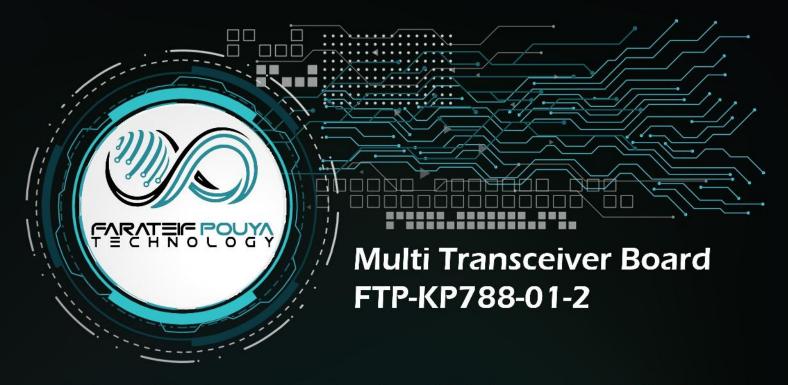
#### Based on Xilinx Kintex-7 XC7K420T-1FFG901I FPGA

- · PCIe Gen2x8
- · Support for both PCI Express and stand-alone modes
- RJ45 for 10/100/1000 Ethernet PHY
- Two SATA Gen 3 supporting data rates up to 6 Gbps.
- USB 2.0 High Speed Host/Device/OTG PHY
- SMA connectors for additional external differential/single clock source.
- 1Gb Micron Embedded flash for fast FPGA configuration and PCI Express enumeration
- 6 channel full duplex RS485/RS422 controlled with PFGA
- 4 channel RS232 controlled with PFGA
- 16MBIT SPI FLASH
- Up to 17 bit GPIO
- 10 LEDs

#### Co-processor ARM Cortex-M4F TM4C1294NCPDTI3R

- 1 channel RS232
- 16MBIT SPI FLASH
- · Two onboard Temperature Sensor
- · Two DIP Switch-4
- ▲ 4 external LED Driver
- 4 External Switch
- 2 onboard Switch
- 8 GPIOs





## 8 channel RF agile transceiver based on 4 RF 2x2 transceiver Analog Devices AD9361

- TX band: 47 MHz to 6.0 GHz
- · RX band: 70 MHz to 6.0 GHz
- Tunable channel bandwidth: <200 kHz to 56 MHz
- · Supports TDD and FDD operation
- Full device configuration and calibration control across dual AD9361 devices
- Phase and frequency synchronization across all AD9361 devices.
- A single external LO source can drive all AD9361s, improving RF performance and easing MIMO design.
  - · Controlled with ARM and FPGA

### Input power supply

- Input voltage range 8 to 14 volts
- · High efficient Switching Pre-Regulators for Linear Voltage.
- Overvoltage Protection
- Reverse Supply Protection
- · Blocks 50Hz and 60Hz AC Power
- Automatic Recovery from Fault

