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FTP-AD9467-FMC-250EBZ-01 16-Bit, 250 MSPS ADC

Introduction

The AD9467 is a 16-bit, monolithic, IF sampling analog-to-digital converter (ADC) with a conversion rate of up to 250 MSPS. The board also provides other options to drive the clock and analog inputs of the ADC.

FEATURES

- 75.5 dBFS SNR to 210 MHz at 250 MSPS
- 90 dBFS SFDR to 300 MHz at 250 MSPS
- Excellent linearity at 250 MSPS
- Integrated input buffer
- 2 V p-p to 2.5 V p-p differential full-scale input
- External reference support option
- Built-in selectable digital test pattern generation
- LVDS outputs (ANSI-644 compatible)

APPLICATIONS

- Multicarrier, multimode cellular receivers
- Antenna array positioning
- Power amplifier linearization
- Broadband wireless
- Radar
- Infrared imaging
- Communications instrumentation

