

QTH-NKS-10

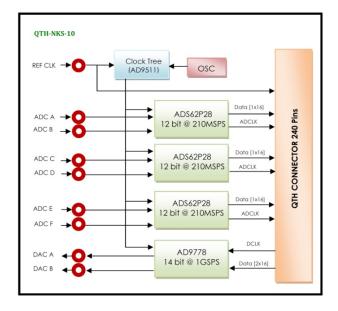
QTH-NKS-10 High Pin Count QTH Module 6- Channel 12-bit ADC 210MSPS 2- Channel 14-bit DAC 1GSPS

Description

The QTH-NKS-10 is a 6-channel 12-bit ADC at 210MSPS and dual-channel 14-bit DAC at 1GSPS that enable simultaneous and high speed sensitivity. This module was designed based on dual-channel ADS62P28 ADC of TI Company with CMOS or DDR LVDS outputs and dual-channel AD9778 DAC of Analog Device Company with CMOS inputs. The clock sampling can be provided from an external or internal source and then send to distribution clock unit. This module can use on every carrier card which follows the QSH/QTH standard; therefore it can have the maximum data throughput and minimum latency.



Functional Block Diagram



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Features

ADC's:

- 6 Channels, 12-bit Resolution, 210 MSPS
- SNR = 73 dBFS @ 70 MHz and 210 MSPS
- SFDR = 86 dBc @ 70 MHz and 210 MSPS
- ENOB = 11.6 Bits @ 70 MHz and 210 MSPS
- Analog Input Filter Options
- Efficient DDR LVDS-Compatible Outputs
- Analog Input: 2V p-p Range
- Xtalk Isolation >91 dB
- Differential Input with 450 MHz Bandwidth

DAC's:

- 2 channels, 14-bit Resolution, 1GSPS DAC Converters
- Analog Output Filter Options
- Interleaved I/Q data for Dual-DAC Mode
- Analog Output: 1V p-p Range
- 3.3V Analog Supply Operation
- 2x, 4x and 8x Interpolation Filters
- Maximum Differential Output with 500 MHz Bandwidth
- High Performance AC-Coupled Analog Output
- SFDR = 80dBFS

Other's:

- Serial Port Control: Programmable Clock and Data Alignment
- On-board Clock Distributer

- QTH Interface Connector for Digital Input Output, Power LEDs and Single-ended MCX Connector for AIN, AOUT& EXTCLK
- Direct ADC/DAC Connection to Host FPGA Ensures
- Maximum Throughput
- Physical Dimensions (L: 113 mm, W: 92.2 mm)
- Industrial Temperature Range (-40°C to 85°C)

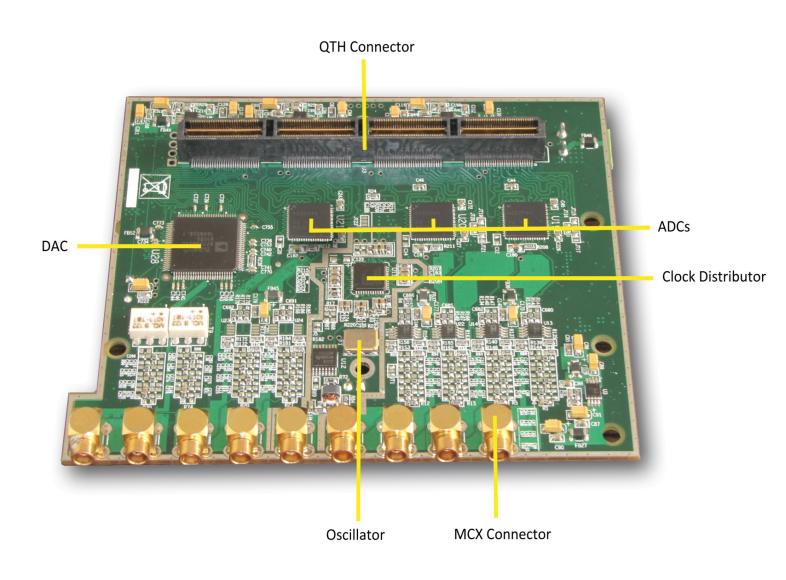
Application

- RADAR/SONAR
- Medical Equipments
- GPS
- Wide Band-Width Communications





Board Features







Compatibility Table

Order Information

Compatibility
Yes

